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ARM (Advanced RISC Machines) originally known as Acorn RISC Machine is a family of reduced instruction set computing (RISC) architecture for computer processors, configured for various environments. Arm holdings is a British company who developed this architecture and licensed it to other companies, who design their own product by using this architecture.

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Keil makes C compilers, macro assemblers, real-time kernels, debuggers, simulators, integrated environments, evaluation boards, and emulators for the Arm, XC16x/C16x/ST10, 251, and 8051 microcontroller families. This web site provides information about our embedded development tools, evaluation software, product updates, application notes, example code, and technical support.

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ARM Compiler version 5 is the reference C/C++ compiler available with a TÜV certified qualification kit for safety applications, as well as long-term support and maintenance. ARM Compiler version 6 is based on the innovative LLVM technology and supports the latest C language standards including C++11 and C++14. It

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Keil uVision This document explains how to build and debug Arm Mbed OS applications using Keil uVision 5. Due to the linker limits, this does not work in the free version of uVision. If you do not have a uVision license, you can use Eclipse, Visual Studio Code or any other IDE that supports debugging through GDB.

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For example, to use the armcc compiler, you need to add the <MKD install dir> /ARM /BIN31 path for uVision3 — or <MKD install dir> /ARM /BIN40 for uVision4 — to the PATH variable. Optionally, you can modify the Compiler Family definition in the project properties and replace the compiler executable names with full paths.

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